Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20030056041"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:13
L2	2	"20030056091"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:14
L3	14	"6438737"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:15
L4	15	"6077315"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:15
S1	. 0	9-218781	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 08:41
S2	7	"9218781"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:29
S3	. 22	"6034538"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2006/12/14 09:06

S4	8	(reconfigur\$4 hardware).ti. AND	US-PGPUB;	ADJ	ON	2006/12/14 09:46
		(command OR instruction) near2 (control OR manager)	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB		•	
S5	29	("4541071"   "4942319"   "5493239"   "5508636"   "5572148"   "5640106"   "5654650"   "5760607"   "5773993"   "5838165").PN. OR ("6034538").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/14 09:10
S6	1	(reconfigur\$4 hardware) AND control (device OR manager) NEAR10(stop OR halt OR activate OR load OR cancel OR wait) NEAR2 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:48
<b>S7</b>	286867	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:54
S8	10580	S7 AND control device	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:50
S9	2035	S7 AND (control device SAME (switch\$3)) AND (circuit model OR netlist OR page OR circuit portion OR tiles OR blocks)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:52
S10	84	S7 AND (control device SAME (switch\$3)) SAME (circuit model OR netlist OR page OR circuit portion OR tiles OR blocks) SAME (activate OR halt OR interrupt OR load OR cancel OR wait)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:53

S11	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:55
S12	1337908	(netlist OR page OR circuit portion OR tiles)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:55
S13	601	S11 AND (S12 NEAR10 load\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:56
S14	470	S11 AND S12 AND (control NEAR5 address NEAR5 (instruction OR command))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:59
S15	116	S11 AND S12 AND (control NEAR5 address NEAR5 (instruction OR command)) AND program memory AND interrupt	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:05
S16	953	S11.ti. AND process\$3 AND memory	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:06
S17	45	S11.ti. AND process\$3 AND memory AND (control NEAR5 instruction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:27

S18	3	"2001331767"	US-PGPUB;	ADJ	ON	2006/12/14 10:27
			USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
S19	22	"1195994"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:28
S20		"11-95994"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:28
S21	2	"09218781"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:30
522	7	"3075166"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:30
S23	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 13:59
S24	41	S23.ti. AND (address (register OR counter OR stack) NEAR5 control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ <sup>*</sup>	ON	2006/12/14 14:04

<del></del>			· · · · · · · · · · · · · · · · · · ·			
S25	. 2	S23.ti. AND page.clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:09
S26	11	S23.ti. AND independent NEAR2 control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:11
S27	124	S23.ti. AND address signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:12
S28	15	S23 AND master parallel	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:34
S29	64	(S23 AND master serial) NOT S28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:21
S30	4	"10319051"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:36
S31	9	"6907595"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:36

		4477			I	·
S32	399	S23 AND cache AND address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:53
S33	0	S23 AND cache SAME address translation SAME \$2configuration data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:53
S34	77	S23 AND cache and address translation and \$2configuration data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:54
S35	0.	S23.ti. AND cache and address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:56
S36	0	S23.ti. AND cache AND address NEAR2 translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:57
S37	0	S23.ti. AND cach\$4 AND address NEAR2 translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:57
S38	64	S23.ti. AND cach\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:58

				,		
S39	0	S23.ti. AND cach\$4 NEAR3 manager AND cach\$4 NEAR3 controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON ·	2006/12/14 15:59
S40	0	S23.ti. AND cach\$4 NEAR3 manager	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:59
S41	16	S23.ti. AND cach\$4 NEAR3 control\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:01
S42	68	S23.ti. AND cach\$	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09
S43	40	"5,600,263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09
544	40	"5600263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09
S45	3	S23.ti. AND interrupt (vector OR command)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:32

S46	81	S23.ti. AND interrupt	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:32
S47	1	S23.ti. AND control flow analysis	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:44
S48	117	"5021947"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:59
S49	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 08:13
S50	106	S49 AND (self reconfiguration OR self re\$1configuration OR selfconfiguration OR self configuration OR self\$1configuration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:17
S51	28	("5684980"   "5802290"   "5838167"   "6094063"   "Re34363").PN. OR ("6326806").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/15 09:49
S52	39	"6091263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 10:12

<u> </u>			I			
S53	5	"6091263" AND cache	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 10:13
S54	482	S49 AND cach\$4 control\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:01
S55	38	S49 SAME cache controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:03
S56	2	"712".clas. AND (microcontroller SAME address SAME relative SAME absolute SAME stack)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:46
S57	0	S49.ti. AND instruction NEAR2 (partition\$3 OR split\$3) NEAR10 control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:47
S58	3	S49.ti. AND instruction NEAR2 (partition\$3 OR split\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:48
S59	108	S49 AND instruction NEAR2 (partition\$3 OR split\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:48

						1
S60	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:23
S61	101	S60 NEAR5 (mux OR multiplexor OR multiplexer) NEAR5 output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:28
S62	150	S60 NEAR5 (mux OR multiplexor OR multiplexer) NEAR5 (output OR input)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:28
S63	286898	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:00
S64		"712".clas. AND S63.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:01
S65		"6721884"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:35
S66	19	"6023755"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55

S67	14	"6145072"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:51
S68		"6172521"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:44
S69	3530	motomura.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON .	2006/12/18 10:45
S70	50	motomura.in. AND (reconfigur\$7 OR \$2programmable)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:16
S71	100	"5684950"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55
S72	. 77	"5684980"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55
S73	173	"712".clas. AND (reconfigur\$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:19

·····		Address Addres	I			1
S74	166	"712".clas. AND (reconfigur\$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3) NOT S64	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ ,	ON	2006/12/19 08:08
S75	153	"712".clas. AND dynamically \$2configurable NOT S73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:35
S76	79	"712".clas. AND dynamically \$2configurable AND S63 NOT S73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S77	0	326/38.clas.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S78	1553	326/38.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:03
S79	2777	326/38.ccls. AND control flow OR instruction flow	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S80	16	326/38.ccls. AND (control flow OR instruction flow)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ _	ON	2006/12/18 11:40

S81	286966	(reconfigurable hardware OR reconfigurable logic OR	US-PGPUB; USPAT;	ADJ	ON	2007/08/09 09:46
		programmable logic or OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
S82	66	"712".clas. AND S81.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:03
S83	55	326/38.ccls. AND (reconfigur\$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3 OR dynamically configurable) NOT S82	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:07
S84	44972	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:09
S85	735	"712".clas. AND S84 AND (control SAME (memory or command or code OR device) SAME address)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:10
S86	173	"712".clas. AND (reconfigur\$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:12
S87		S86 AND cache AND address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON .	2006/12/19 08:11

· · · · · · ·						
S88	90	"712".clas. AND (reconfigur\$6 hardware) AND (partition\$3 OR split\$3 OR separat\$3 OR divid\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:14
S89	13	"712".clas. AND (self reconfiguration OR self re\$1configuration OR selfconfiguration OR self configuration OR self\$1configuration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:20
S90	121	abramovici.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:20
S91	14	("4541071"   "5452231"   "5574388"   "5604888"   "5644515"   "5831996"   "5903744"   "6009256"   "6292916").PN. OR ("6442732"). URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/19 08:24
S92	42	(plurality NEAR3 S84) SAME (\$5controller OR \$5control) SAME (instruction OR command)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/19 08:26
S93	29	("712".clas. OR 326/38.ccls.) AND (runtime reconfiguration OR run-time reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 09:42
S94	36	"712".clas. AND (reconfigurable comput\$3) AND (instruction sequence OR instruction flow OR control flow OR control sequence OR microoperations OR microops OR micro-ops)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 09:43
S95	2	"712".clas. AND opcode NEAR3 (flag OR bit) NEAR3 (following or next OR present) NEAR3 (execut\$3 OR stop\$3 OR ended)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:46

505		1171011 - L. AND	HC DCDHB	AD1	ON	2006/12/10 10:16
S96	0	"712".clas. AND opcode NEAR3 (flag OR bit) NEAR3 (break)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:46
S97	1	"712".clas. AND opcode NEAR3 (flag OR bit OR parameter) NEAR3 (stop\$3 OR halt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:47
S98	86	"712".clas. AND (flag OR bit OR parameter) NEAR3 (following OR next OR subsequent) (instruction OR command) NEAR3 (execut\$3 OR run\$3 OR ignor\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:53
S99	0	"712".clas. AND thread NEAR3 (stop\$3 OR terminat\$3 OR end\$3) bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:57
S10 0	43	"712".clas. AND nop NEAR3 bit NEAR3 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:01
S10 1	64	"712".clas. AND (stop OR halt) NEAR3 (parameter OR bit) NEAR3 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:03
S10 2	361	"712".clas. AND (stop OR halt) NEAR3 (parameter OR bit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:04

S10 3	216	"712".clas. AND (stop OR halt) (parameter OR bit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	ADJ	ON	2006/12/19 11:08
S10 4	90	"712".clas. AND (stop OR halt) (parameter OR bit) SAME instruction	IBM_TDB  US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:38
S10 5	44972	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:39
S10 6	89	S105 AND (cache control\$3) AND (address translation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:40
\$10 7	0	S105.ti. AND (cache control\$3) AND (address translation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:42
S10 8	7	S105.ti. AND (cache control\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:56
S10 9		S105.ti. AND (cache control\$3) AND translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:43

				4.5.5	011	2006/42/42 42 42
S11 0	0	S105.ti. AND (cache control\$3) AND TLB	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:43
S11 1	0	S105.ti. AND (cache control\$3) AND virtual	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:56
S11 2	126	(cache controller SAME address translat\$3 SAME cache)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 14:00
S11 3		S105.ti. AND MMU AND translat\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 14:00
S11 4	25	("6085317").URPN.	USPAT	ADJ	ON	2006/12/19 16:54
S11 5	1	2001-331767	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:48
S11 6	1	2001-147802	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:48
S11 7	11	"3099889"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49

S11 8	0	200-181566	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S11 9	3	2000-181566	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2006/12/20 10:49
S12 0	1	2000-040745	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S12 1	0	2000-040745 A	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S12 2	0	2000-040745A	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S12 3	1	11-184718	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49

S12 4	78	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD OR EEPLD OR Logic Cell Arrays OR LCA) SAME divi\$4 SAME program SAME (processing unit OR logic block Or logic unit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 09:56
S12 5	<b>71</b>	(program NEAR7 (divid\$4 OR partition\$3)) NEAR10 reconfigur\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:23
S12 6		"3812469"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:02
S12 7	10	"6658564"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:27
S12 8	3	"6637017"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:28
S12 9	9	"6633181"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:29

·			- T			[
S13 0	14	"6438737"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:32
S13 1	15	"6077315"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:40
S13 2	2	"20030056091"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 11:15
S13 3	10	"6658564"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 11:15